

Research Intern

Manipal Academy of Higher Education (MAHE) invites application for the position of Research Intern to work for the DST (SERB- ACCELERATE VIGYAN SCHEME) funded project titled "Estimation of Shalaka Temperature and Tapping Pressure" in the Department of Electronics and Communication Engineering, Manipal Institute of Technology (MIT), MAHE, Manipal, India.

Qualification	Postgraduate in M.E./MTech in VLSI/ Microelectronics/ Electronics and Communication/ Electrical and Electronics /Instrumentation/Embedded Systems/ Biomedical/Related Circuit Branches/M.sc (Electronics)/PhD in above mentioned specializations. Desirable - Preference will be given to candidates with above qualification and having work experience in Electronic Circuit Design.
Duration	2 Months
Remuneration	A consolidated amount for daily necessary expenses such as TA, stationary, consumables, accommodation, food for the participating students will be provided as per DST(SERB) norms
Posts	2

Candidates possessing the requisite qualification may send detailed CV with copies of relevant certificates in support of the qualification, experience and a letter of authentication from their Supervisor / Head of the Department / Head of the Institute indicating their association with the Institute and No Objection Certificate (NOC)" to undergo internship, if selected to the following email id / address by April 15, 2022. No TA/DA will be paid for attending the interview. Shortlisted candidates will be informed via email to appear for the interview. Probable date of joining May 01, 2022 or June 01 2022



MANIPAL
ACADEMY of HIGHER EDUCATION

(Deemed to be University under Section 3 of the UGC Act, 1956)

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